

CS152
Computer Architecture and Engineering
Lecture 24

Busses (continued)
Queueing Theory
Disk IO

November 28, 2001

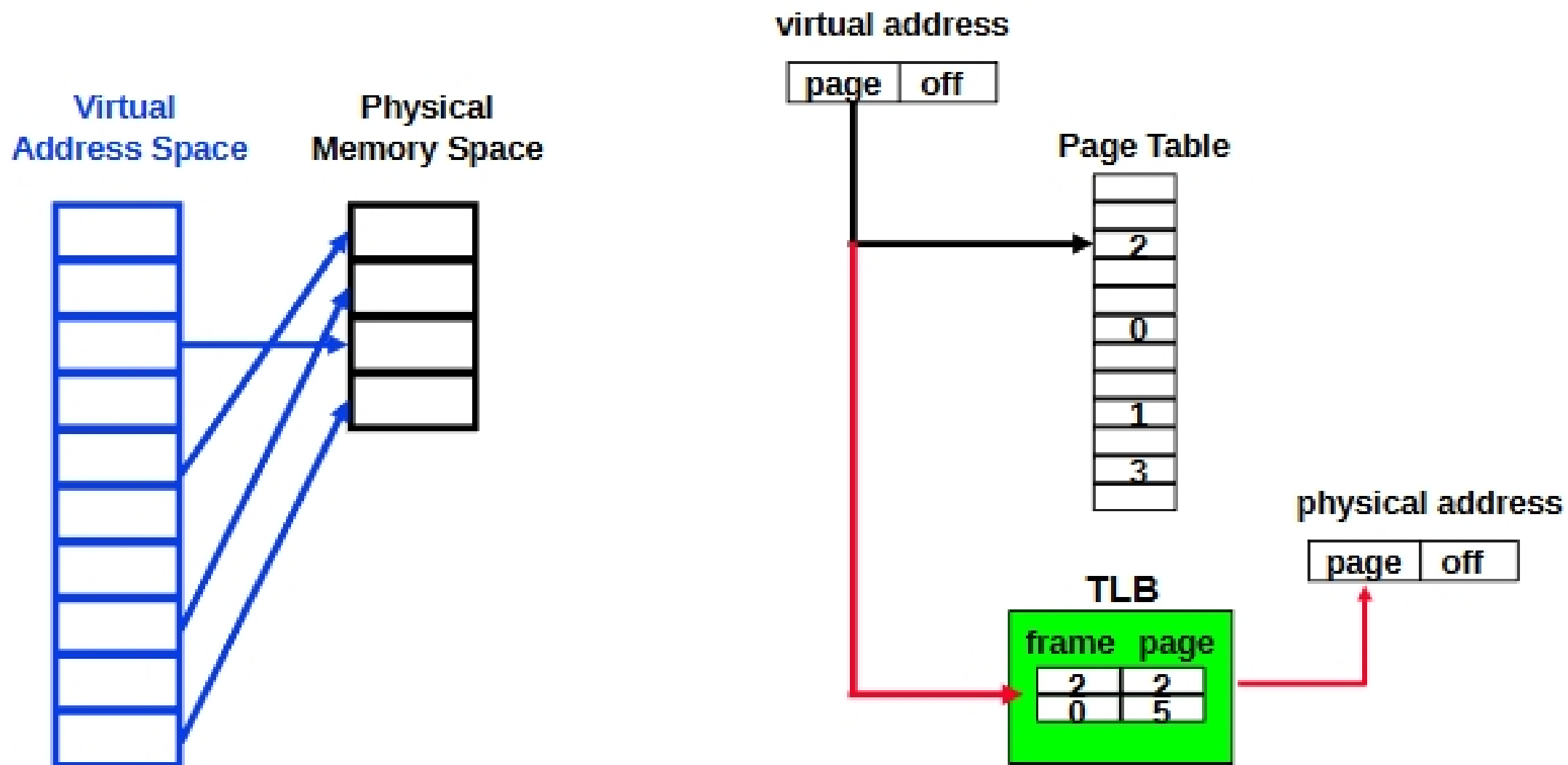
John Kubiatoiwicz (<http://cs.berkeley.edu/~kubitron>)

lecture slides: <http://www-inst.eecs.berkeley.edu/~cs152/>

Recap: Making address translation practical:

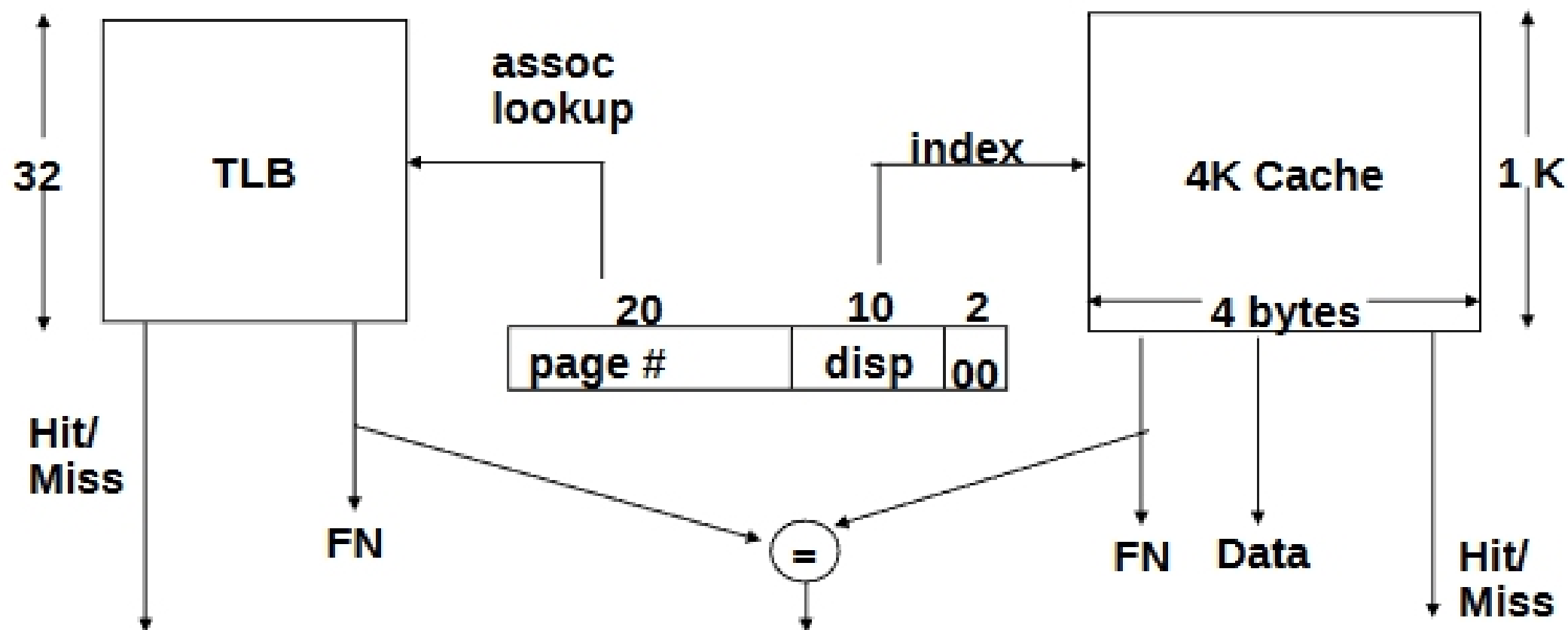
TLB

- Virtual memory => memory acts like a cache for the disk
- Page table maps virtual page numbers to physical frames
- Translation Look-aside Buffer (TLB) is a cache translations



Recap: Overlapped TLB & Cache Access

- If we do this in parallel, we have to be careful, however:



What if cache size is increased to 8KB?