

COP 5611 Operating Systems Spring 2010

Dan C. Marinescu

Office: HEC 439 B

Office hours: M-Wd 2:00-3:00 PM

Lecture 7

- Last time: Thread coordination
- Today:
 - Thread coordination
 - Scheduling
 - Multi-level memories
 - I/O bottleneck
- Next Time:

Hardware support for atomic actions

- RSM (Read and Set Memory) instruction
- TST (Test and Set) instruction
- Two primitives:
 - ACQUIRE (lock)
 - RELEASE (lock)

use atomic instructions to manipulate the lock.