University of California, Berkeley
College of Engineering
Computer Science Division — EECS

Spring 2001
Handout #1

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Course Information
CS152 Computer Architecture and Engineering

Lectures:	T/Th 2:00 - 3:30, Room 306 Soda
Discussion 101:	We 2:00 - 4:00, Location TBA
Discussion 102:	We 4:00 - 6:00, Location TBA
Laboratory:	119 Cory Hall and 273 Soda Hall

Course Web Site:

For the laboratory assignments we will primarily use the workstations in 119 Cory Hall. The first couple of labs will be on UNIX workstations (such as in 273 Soda hall). You should be able to access your home directory on any of the instructional computers. However, the CAD tool, Workview, will only run on the NT workstations in 119 Cory Hall. Although the freedom to work outside the lab is valuable, so is the collective environment of the lab, so we encourage you to do some of each.

Course Grading:

The CS Division guideline for an upper division CS class is that the overall class GPA should be between 2.7 and 3.1. Thus the average grade in this class will be a B or B+; please set your expectations accordingly.

There will be two midterm exams covering the material from the readings and class—and no final. They will be given over a 3-hour period in the evening to reduce time pressure: consult the attached schedule to see the date, time, and location.

Two Midterm Exams:	35%	combined
Labs and Design Project:	35%	
Homework Completion:	5%	
Quizzes:	15%	
Project Group Participation:	5%	
Class and Discussion Section Participation:	5%	

Homework and Lab Policy:

Homework exercises and lab assignments will be typically available on Wednesdays, with homework exercises due at the beginning of class on Wednesday (1 or 2 weeks later). **No late homeworks will be accepted.** There will be a short 15 minute homework quiz one days in which homework is due.

Lab assignments are due on Fridays by 5pm in the wooden box in 283 Soda Hall.

Final projects will be presented to the course staff at the end of the term, with a written project due the following week. Consult the attached schedule for due dates of homework exercises, labs, and projects.

Course Materials:

Required	Patterson and Hennessy, *Computer Organization & Design: The Hardware/Software Interface* (second edition), Morgan Kaufman Publishers, Inc., San Mateo, CA 1997.
Recommended	G. Kane & J. Heinrich, *MIPS RISC Architecture*, Prentice Hall, 1992,
Recommended	*CS152 1992 Project Reports*, on reserve in Engineering Library.

There is also a set of Powerview Reference Manuals in the lab and web page has copies of all lecture slides and handouts.