



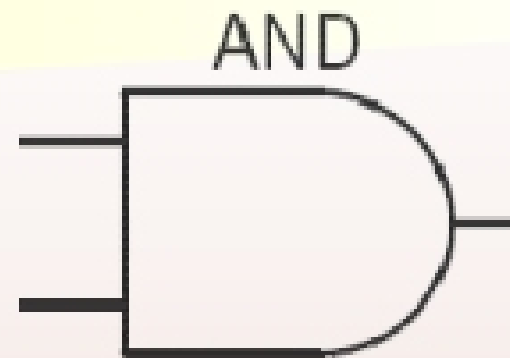
CMSC 250

Discrete Structures

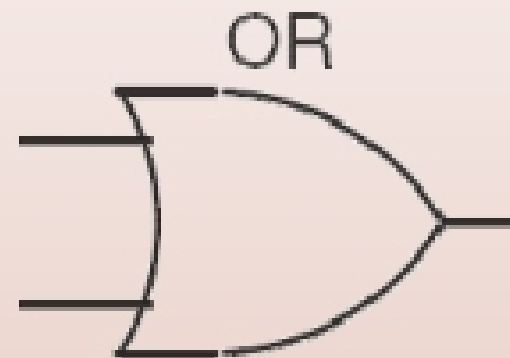
Logic Applications
(Circuits & Adders)

Circuits

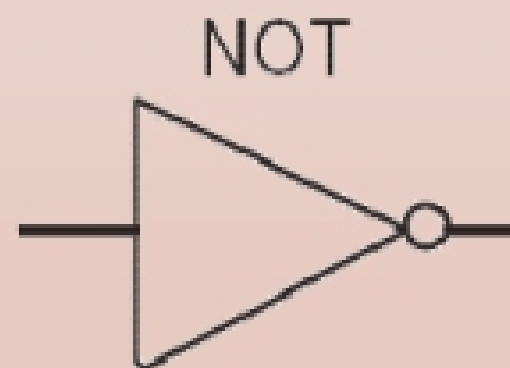
■ AND gate



■ OR gate



■ NOT gate



Combining & Determining I/O Relationship

■ $P \vee \sim (Q \wedge R)$

