

Announcements

- Midterm #1
 - Re-grade requests due by end of class today
- Project #3
 - Is out
 - Deadline is shortly after midterm #2 (start early)

Project #3

- What is pageable?
 - User memory including text, data, and stack
- Memory model
 - Kernel memory in low memory
 - User memory in high memory
- Paging Bits
 - cr3 – Page Table Base Register (PTBR)
 - cr0:31 – Enable Paging bit
 - cr2 – Address causing page fault
- Page Faults
 - Look in errorCode fields of interrupt

Working Sets and Page Replacement

- Programs usually display reference locality
 - temporal locality
 - repeated access to the same memory location
 - spatial locality
 - consecutive memory locations access nearby memory locations
 - memory hierarchy design relies heavily on locality reference
 - sequence of nested storage media
- Working set
 - set of pages referenced in the last delta references

