

Lecture 34: Designing amplifiers, biasing, frequency response

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Context

We will figure out more of the design parameters for the amplifier we looked at in the last lecture, and then we will do a review of the approximate frequency analysis of circuits which have a single dominant pole.

Reading

- Chapter 9, multi-stage amplifiers. The frequency analysis is in the first section of chapter 10, but we won't go farther into chapter 10 for a while.
- The Lectures on Wednesday and Friday will be given by Joe and Jason, respectively. They will be doing several example problems.

Lecture Outline

- Example 1: Cascode Amp Design
- Example 2; CS NMOS->CS PMOS
- Review of frequency analysis (with a dominant pole)

