

EE 200

Design Tools

Laboratory 2

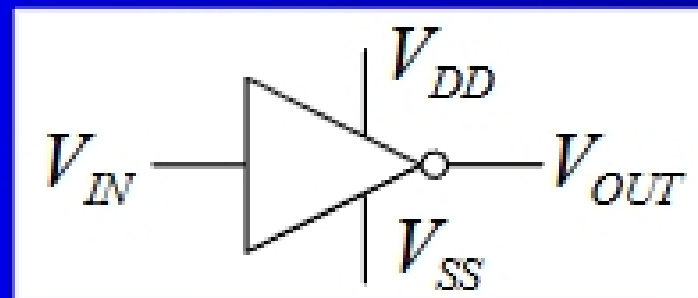
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Laboratory 2 Topics

- Review Material
 - EE 210 and CMPEN 270
- LabVIEW
 - Review
 - Coercion Dots and Numeric Conversion
 - Function Nodes

Exercise 1

- Sketch the typical input-output transfer characteristic, V_{OUT} versus V_{IN} , of the CMOS inverter



- Indicate on the diagram
 - V_{OHmin} : minimum output voltage in the HIGH state
 - V_{IHmin} : minimum input recognized as a HIGH
 - V_{ILmax} : maximum input recognized as a LOW
 - V_{OLmax} : maximum output voltage in the LOW state