
CS 152
Computer Architecture and Engineering
Lecture 2 – Single Cycle Datapaths

2005-1-20

John Lazzaro

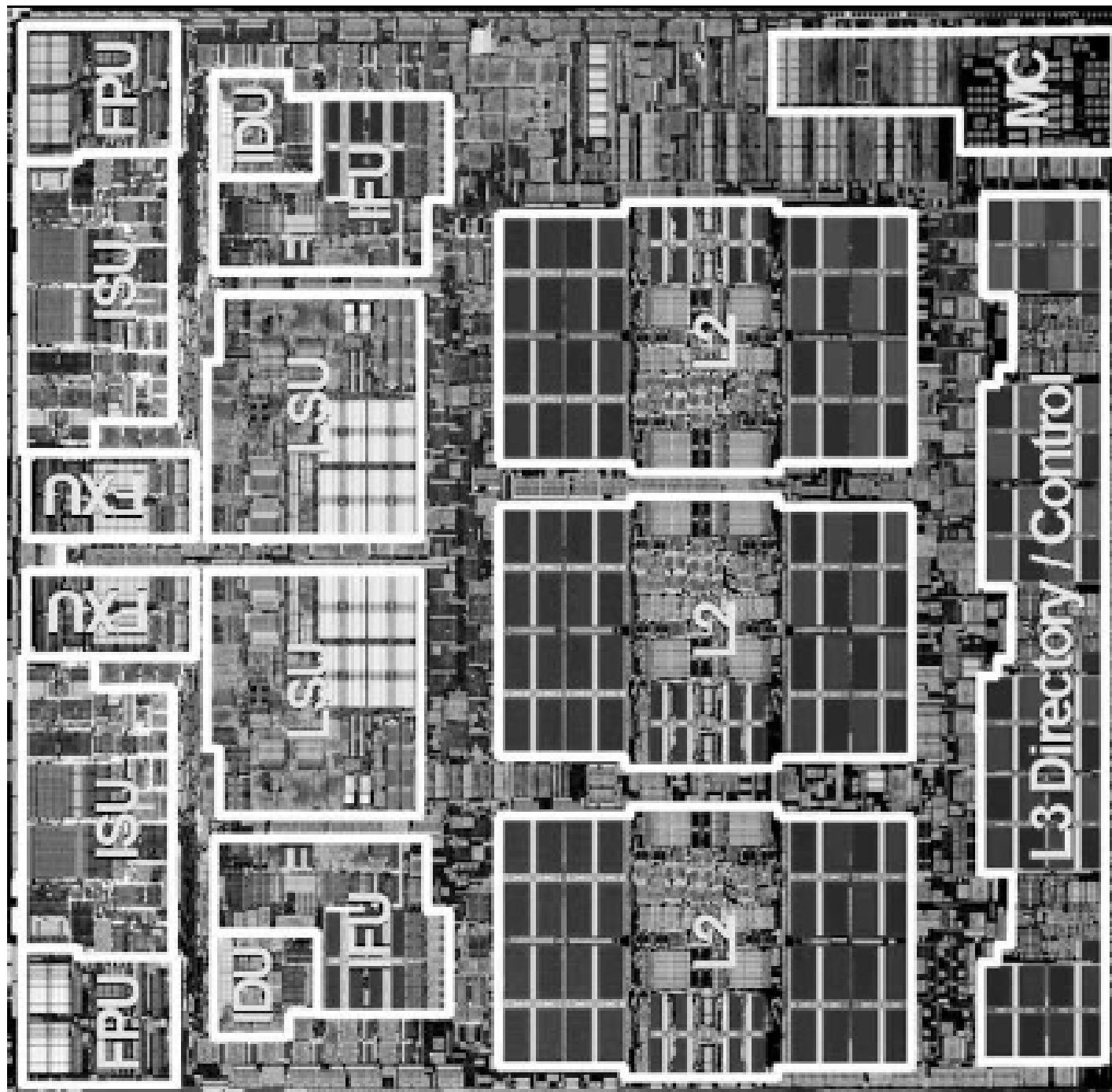
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Last Time: CS 152 Course Introduction



**Single-cycle
CPU project
3 weeks**

**Pipelined CPU
4 weeks**

**Final Project
5 weeks
200 hr/student**

**Teams of
4-5 students**



IBM Power 5 "die photo": a die is an unpackaged part

CS 152 L2: Single Cycle Datapaths

UC Regents Spring 2005 © UCB

Today: Single Cycle Datapath Design

The book presentation of single cycle processors is sufficient to do Lab 2.

1	Th 1/20	Single-Cycle Datapaths		5.1-4, 5.8, Appendix B
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This lecture is not.

This lecture is a gentle introduction, to prepare you to read the book ...

