

# EECS 373

## Design of Microprocessor-Based Systems

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Lecture 7: Interrupts, ARM NVIC  
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"PLEASE FEEL FREE TO INTERRUPT  
IF YOU HAVE A QUESTION."



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## Minute Quiz...

# Recap of the last lecture



- Why is Reset Vector +1?
  - It's an ARM specific thing. The least significant bit in jump instructions indicates the type of instruction at that location (0: for ARM, 1: for Thumb). Since the Cortex-M3 can only execute Thumb2, this will always