
CS 152

Computer Architecture and Engineering

Lecture 1 – The MIPS ISA

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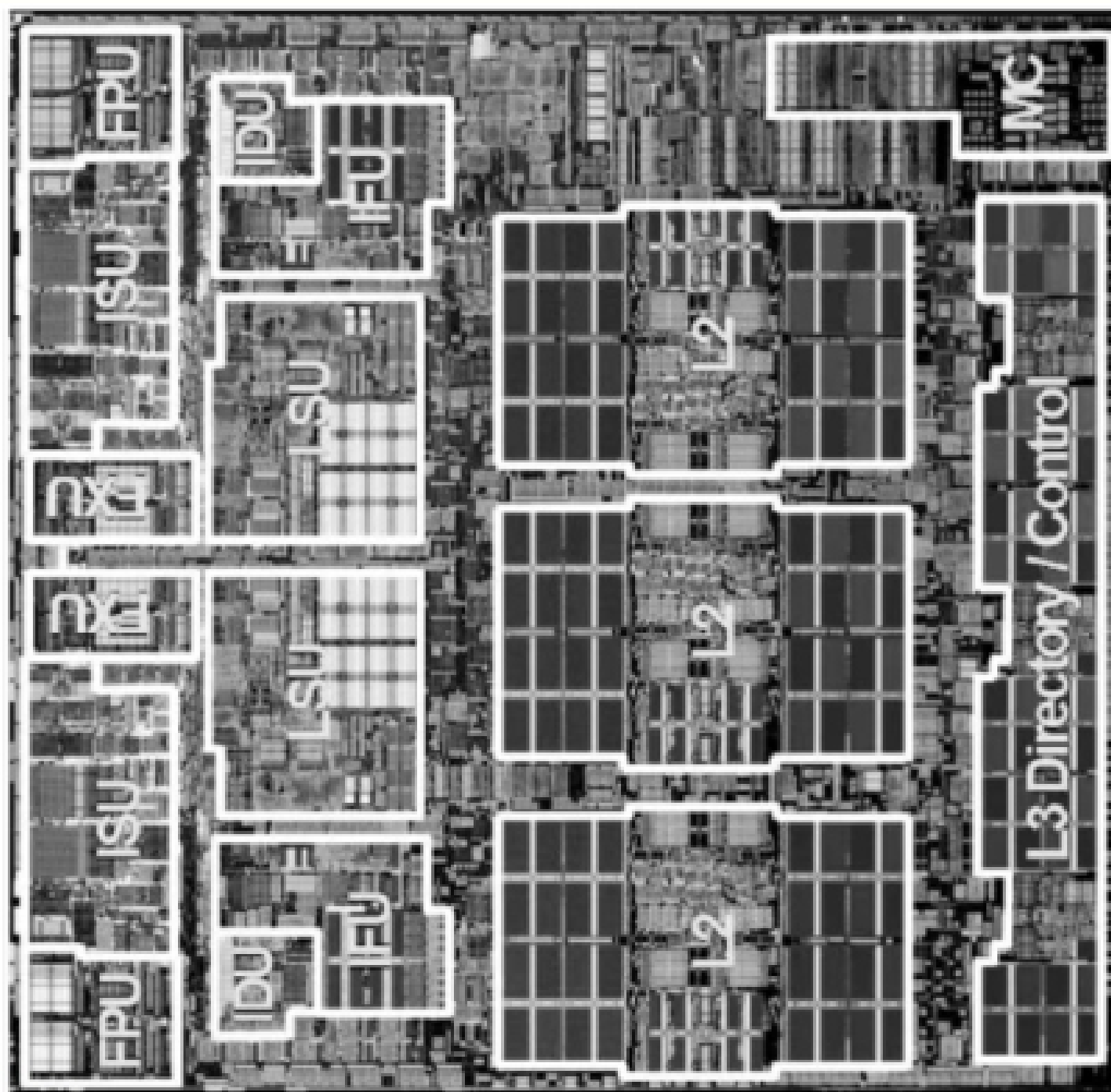
*And also,
an intro
to the
course.*

**TAs: Udam Saini and
Joe Sun**

www-inst.eecs.berkeley.edu/~cs152/



CS 152: Computer Design Team Projects



**Single-cycle
CPU project
3 weeks**

**Pipelined CPU
3 weeks**

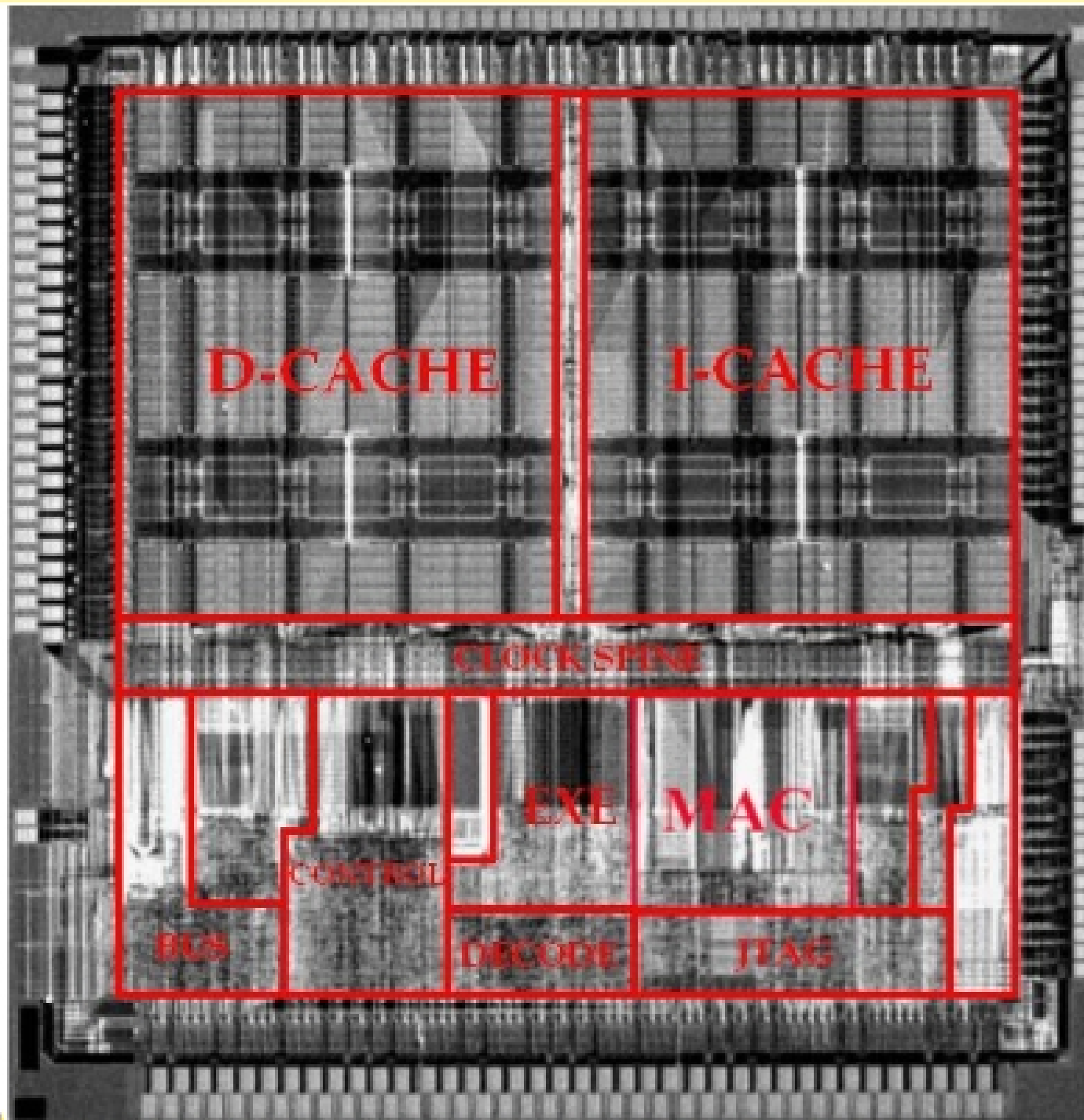
**Final Project
5 weeks**

**Teams of
4-5 students**



IBM Power 5 "die photo": a die is an unpackaged part

CS 152: Real hardware, not simulation



Will we be
fabricate
CPU dies?

Back when I
was taking
classes (1984
@ Caltech)
our project
course did fab
chips.

Intel XScale 80200: used in earlier HP PocketPCs