

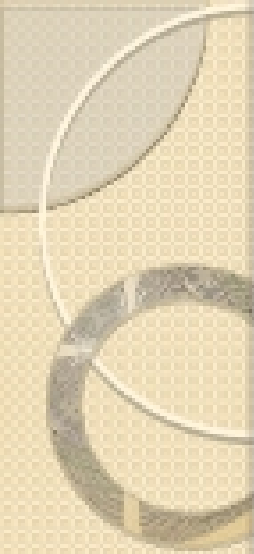


A Building Block Approach to Sensornet Systems

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- What is the paper about?
 - ✓ Proposes to reduce the hardware platform development time-to-result
 - ✓ Time-to-result= Prototype+Pilot+Production

 - What it proposes?
 - ✓ Building block approach to the hardware platform

 - How to go about it?
 - ✓ Partitioning functionality between modules and carriers, and identify guidelines for their interconnection
 - ✓ Architecture should support the 3 stages of design

BACKGROUND

- Earlier approaches remain inadequate
- Did not answer all stages
- Examples for ineffective approaches
- Mica2Dot, Telos, iMote, BTnode, Eyes, TIP, TinyNode, Sensinode, IRIS, MICAz Stamp, and kMote, MASS, and mPlatform