
EECE 276

Embedded Systems

HC 12 Overview: Interface devices
ICE/LAN debugging tools

HC12 Interfaces

- 9S12DP256: Complex HC12 system
 - » HC12 here means: 9S12DP256
- On-chip memory
 - » EEPROM (4K)
 - » RAM (12K)
 - » Flash (256K)- tricky to access
- Various operating modes (see ref. guide)

HC12 Interfaces

- Enhance Capture Timer
 - » Similar to HC11 timer unit:
 - Input capture, output compare, pulse accumulator
- Serial Communication Interface
 - » DUART – Asynchronous (w/o clock line)
- Serial Peripheral Interface
 - » Synchronous, serial port
- Inter-IC Bus
 - » 2-wire, bidirectional serial bus (100KBPS)