

**Data values in 68HC11 registers.**

Recall that registers A and B are 8-bit registers and that registers D, X, and Y are 16-bit registers. Since the registers contain *signed binary numbers* note that:

- If the MSB = 0, the value is positive
- If the MSB = 1, the value is negative and in 2's complement form

|    |   |    |   |   |   |                             |   |   |                         |
|----|---|----|---|---|---|-----------------------------|---|---|-------------------------|
| 7  | A | 0  | 7 | B | 0 | 8-Bit accumulators A and B  |   |   |                         |
| 15 |   | D  |   |   | 0 | 16-Bit double accumulator D |   |   |                         |
| 15 |   | X  |   |   | 0 | Index register X            |   |   |                         |
| 15 |   | Y  |   |   | 0 | Index register Y            |   |   |                         |
| 15 |   | SP |   |   | 0 | Stack pointer               |   |   |                         |
| 15 |   | PC |   |   | 0 | Program Counter             |   |   |                         |
|    | S | X  | H | I | N | Z                           | V | C | Condition Code Register |

**Example:** Determine the value stored in each register below in decimal form:

**Register A**

0 0 0 1 1 0 0 1



\_\_\_\_\_ (decimal value)

**Register B**

1 1 0 1 1 1 0 0



\_\_\_\_\_ (decimal value)

**Register X**

0 0 0 1 0 0 0 0 0 0 0 0 0 0 1 1 0



\_\_\_\_\_ (decimal value)

**Register X**

1 1 1 1 1 1 1 0 1 1 1 1 0 0 1 1



\_\_\_\_\_ (decimal value)

**Example:** Determine the largest and smallest (most negative) values that can be stored in registers A and X.

| Register | Largest Value | Smallest value |
|----------|---------------|----------------|
| A        |               |                |
| X        |               |                |

**Memory Addresses in 68HC11 registers.**

Note that memory addresses are NOT *signed binary numbers* so no sign bit is used.

**Example:**

So if an 8-bit memory address is specified (2 hexadecimal digits) as in the examples below, what is the largest and smallest memory address?

STAA \$00 → memory address = \_\_\_\_\_ (decimal form)

STAA \$FF → memory address = \_\_\_\_\_ (decimal form)

total amount of memory addresses with 8 bits: \_\_\_\_\_

**Example:**

So if an 16-bit memory address is specified (4 hexadecimal digits) as in the examples below, what is the largest and smallest memory address?

STAA \$0000 → memory address = \_\_\_\_\_ (decimal form)

STAA \$FFFF → memory address = \_\_\_\_\_ (decimal form)

total amount of memory addresses with 16 bits: \_\_\_\_\_