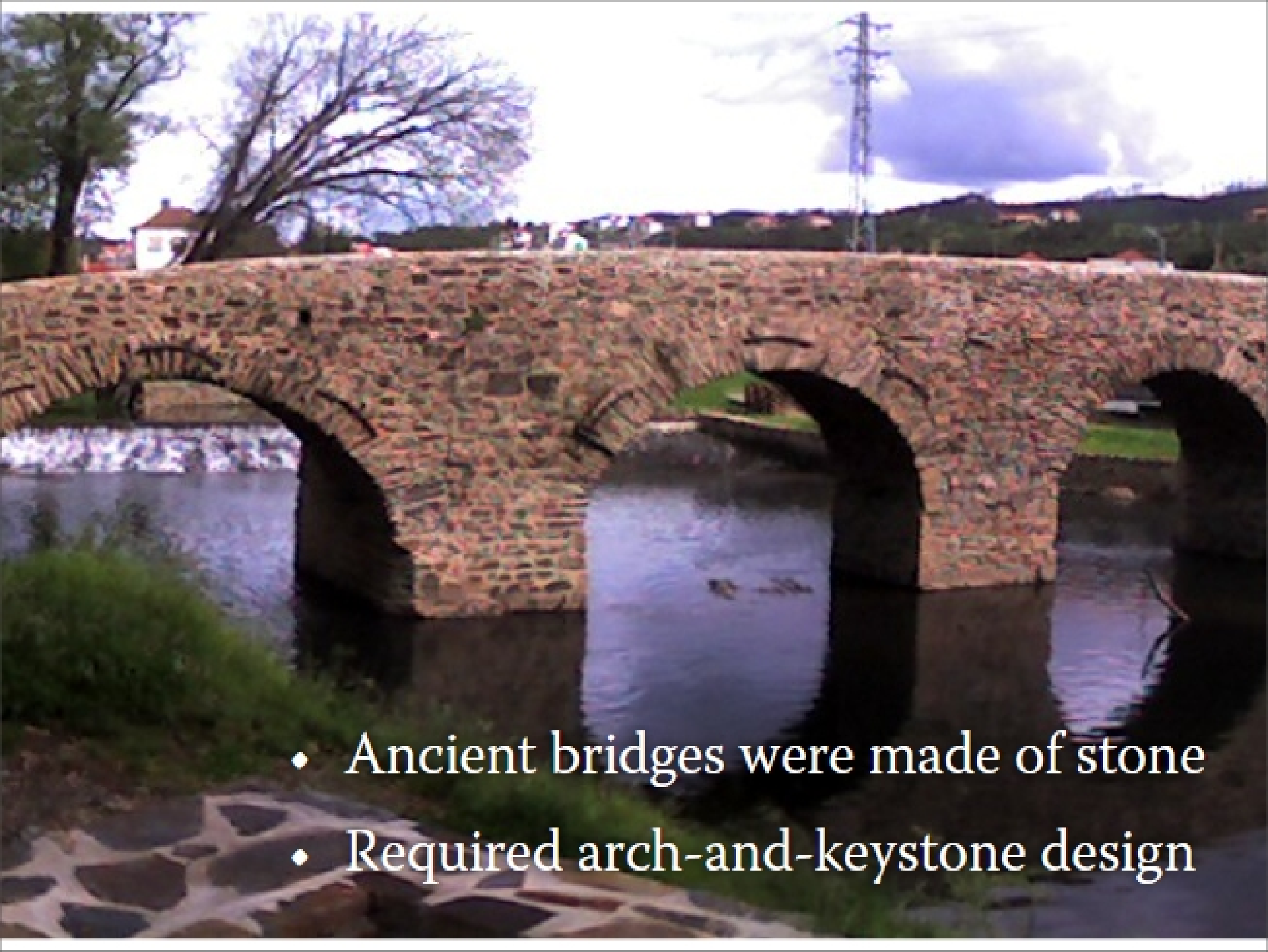


An Overview of  
**FLEET**

CS-152

# FLEET

- Brainchild of Ivan Sutherland
  - Fleshed out in collaboration with Berkeley graduate students
- A one-instruction, clockless processor
  - Alternatively: an asynchronous transport-triggered VLIW machine
- Designed to take advantage of asynchronous circuits
- But first, I'd like to talk about...



- Ancient bridges were made of stone
- Required arch-and-keystone design