

Low Power via Sub-Threshold Circuits

Mike Pridgen

- “Logic Circuits Operating in Subthreshold Voltages”
 - Jabulani Nyathi and Brent Bero
- “Sub-Threshold Design: The Challenges of Minimizing Circuit Energy”
 - B.H. Calhoun, A. Wang, N. Verma, and A. Chandrakasan

Goal of Sub-Threshold Circuits

- Minimize energy
 - Utilize leakage currents
 - Sacrifice speed