

# CS152 – Computer Architecture and Engineering

## Lecture 4 – Timing

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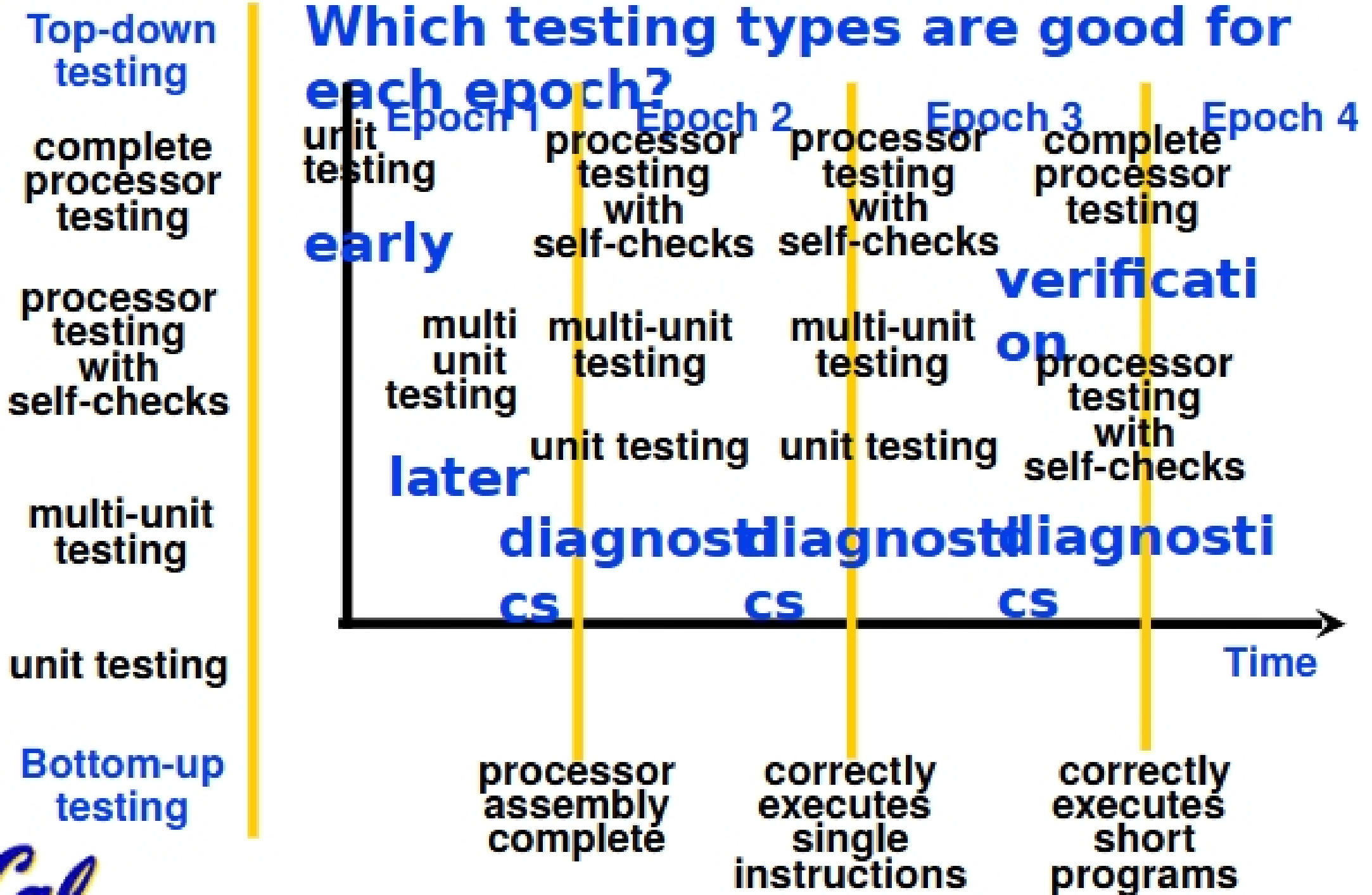
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# Last Time: Test plan for your project



# Outline - Timing

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\* **A clocked logic **circuit** primer**

\* **Team networking break!**

\* **More clocked logic **circuits****